

If you have one of ESP32-C3 official development boards listed below, you can click on the link to learn more about the hardware.

ESP32-C3-DevKitM-1

This user guide will help you get started with ESP32-C3-DevKitM-1 and will also provide more in-depth information.

ESP32-C3-DevKitM-1 is an entry-level development board based on [ESP32-C3-MINI-1](#), a module named for its small size. This board integrates complete Wi-Fi and Bluetooth LE functions.

Most of the I/O pins on the ESP32-C3-MINI-1 module are broken out to the pin headers on both sides of this board for easy interfacing. Developers can either connect peripherals with jumper wires or mount ESP32-C3-DevKitM-1 on a breadboard.

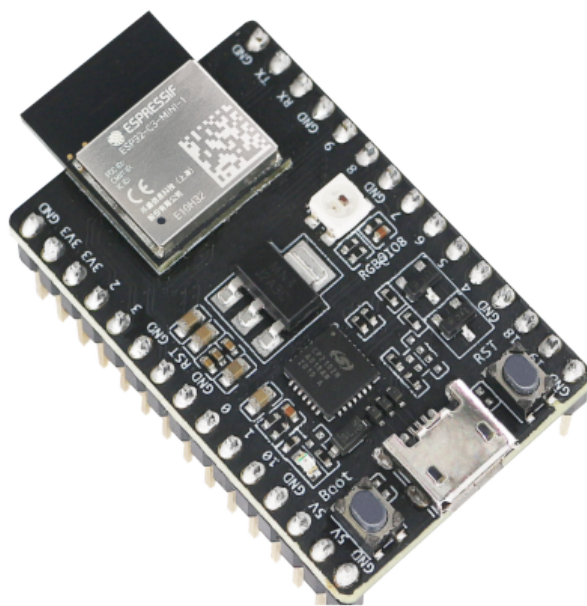


Fig. 1: ESP32-C3-DevKitM-1

The document consists of the following major sections:

- [Getting Started](#): Overview of ESP32-C3-DevKitM-1 and hardware/software setup instructions to get started.
- [Hardware Reference](#): More detailed information about the ESP32-C3-DevKitM-1's hardware.
- [Hardware Revision Details](#): Revision history, known issues, and links to user guides for previous versions (if any) of ESP32-C3-DevKitM-1.
- [Related Documents](#): Links to related documentation.

Getting Started This section provides a brief introduction of ESP32-C3-DevKitM-1, instructions on how to do the initial hardware setup and how to flash firmware onto it.

Description of Components The key components of the board are described in a counter-clockwise direction.

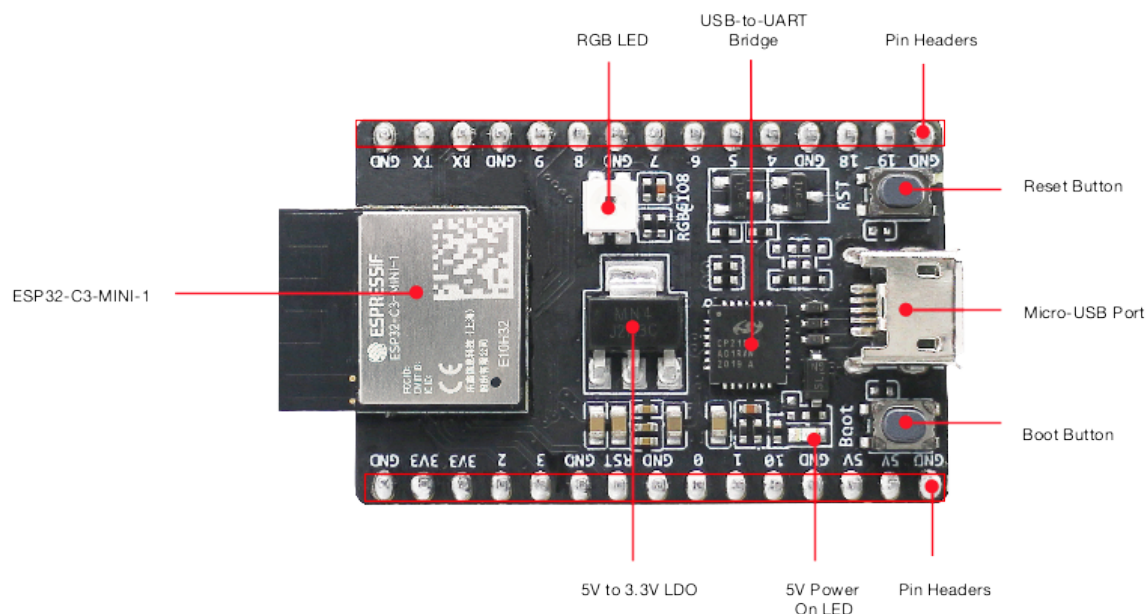


Fig. 2: ESP32-C3-DevKitM-1 - front

Key Component	Description
ESP32-C3-MINI-1	ESP32-C3-MINI-1 is a general-purpose Wi-Fi and Bluetooth LE combo module that comes with a PCB antenna. At the core of this module is ESP32-C3FN4 , a chip that has an embedded flash of 4 MB. Since flash is packaged in the ESP32-C3FN4 chip, rather than integrated into the module, ESP32-C3-MINI-1 has a smaller package size.
5 V to 3.3 V LDO	Power regulator that converts a 5 V supply into a 3.3 V output.
5 V Power On LED	Turns on when the USB power is connected to the board.
Pin Headers	All available GPIO pins (except for the SPI bus for flash) are broken out to the pin headers on the board. For details, please see Header Block .
Boot Button	Download button. Holding down Boot and then pressing Reset initiates Firmware Download mode for downloading firmware through the serial port.
Micro-USB Port	USB interface. Power supply for the board as well as the communication interface between a computer and the ESP32-C3FN4 chip.
Reset Button	Press this button to restart the system.
USB-to-UART Bridge	Single USB-UART bridge chip provides transfer rates up to 3 Mbps.
RGB LED	Addressable RGB LED, driven by GPIO8.

Start Application Development Before powering up your ESP32-C3-DevKitM-1, please make sure that it is in good condition with no obvious signs of damage.

Required Hardware

- ESP32-C3-DevKitM-1
- USB 2.0 cable (Standard-A to Micro-B)
- Computer running Windows, Linux, or macOS

Note: Be sure to use an appropriate USB cable. Some cables are for charging only and do not provide the needed data lines nor work for programming the boards.

Software Setup Please proceed to [Get Started](#), where Section [Installation](#) will quickly help you set up the development environment and then flash an application example onto your ESP32-C3-DevKitM-1.

Contents and Packaging

Retail Orders If you order one or several samples, each ESP32-C3-DevKitM-1 comes in an individual package in either antistatic bag or any packaging depending on your retailer.

For retail orders, please go to <https://www.espressif.com/en/contact-us/get-samples>.

Wholesale Orders If you order in bulk, the boards come in large cardboard boxes.

For wholesale orders, please go to <https://www.espressif.com/en/contact-us/sales-questions>.

Hardware Reference

Block Diagram The block diagram below shows the components of ESP32-C3-DevKitM-1 and their interconnections.

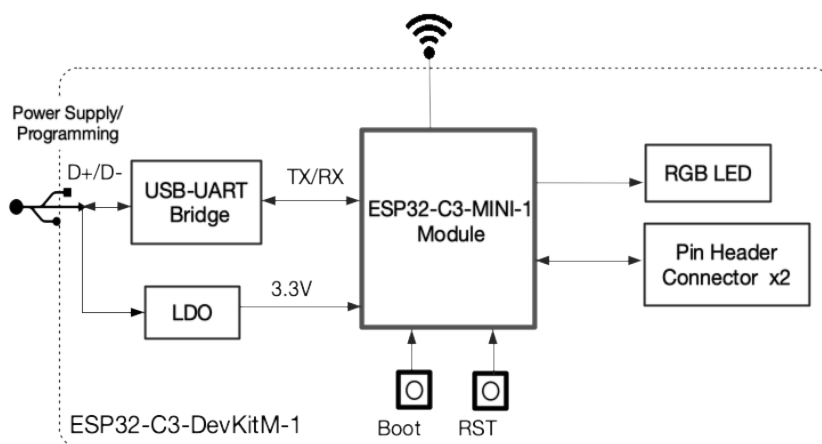


Fig. 3: ESP32-C3-DevKitM-1 (click to enlarge)

Power Supply Options There are three mutually exclusive ways to provide power to the board:

- Micro-USB Port, default power supply
- 5V and GND pin headers
- 3V3 and GND pin headers

It is recommended to use the first option: Micro-USB Port.

Header Block The two tables below provide the **Name** and **Function** of the pin headers on both sides of the board (J1 and J3). The pin header names are shown in [ESP32-C3-DevKitM-1 - front](#). The numbering is the same as in the [ESP32-C3-DevKitM-1 Schematic \(PDF\)](#).

J1

No.	Name	Type ^{Page 1}	Function
1	GND	G	Ground
2	3V3	P	3.3 V power supply
3	3V3	P	3.3 V power supply
4	IO2	I/O/T	GPIO2 ² , ADC1_CH2, FSPIQ
5	IO3	I/O/T	GPIO3, ADC1_CH3
6	GND	G	Ground
7	RST	I	CHIP_PU
8	GND	G	Ground
9	IO0	I/O/T	GPIO0, ADC1_CH0, XTAL_32K_P
10	IO1	I/O/T	GPIO1, ADC1_CH1, XTAL_32K_N
11	IO10	I/O/T	GPIO10, FSPICS0
12	GND	G	Ground
13	5V	P	5 V power supply
14	5V	P	5 V power supply
15	GND	G	Ground

J3

No.	Name	Type ⁷	Function
1	GND	G	Ground
2	TX	I/O/T	GPIO21, U0TXD
3	RX	I/O/T	GPIO20, U0RXD
4	GND	G	Ground
5	IO9	I/O/T	GPIO9 ²
6	IO8	I/O/T	GPIO8 ² , RGB LED
7	GND	G	Ground
8	IO7	I/O/T	GPIO7, FSPID, MTDO
9	IO6	I/O/T	GPIO6, FSPICLK, MTCK
10	IO5	I/O/T	GPIO5, ADC2_CH0, FSPIWP, MTDI
11	IO4	I/O/T	GPIO4, ADC1_CH4, FSPIHD, MTMS
12	GND	G	Ground
13	IO18	I/O/T	GPIO18, USB_D-
14	IO19	I/O/T	GPIO19, USB_D+
15	GND	G	Ground

Pin Layout

Hardware Revision Details No previous versions available.

Related Documents

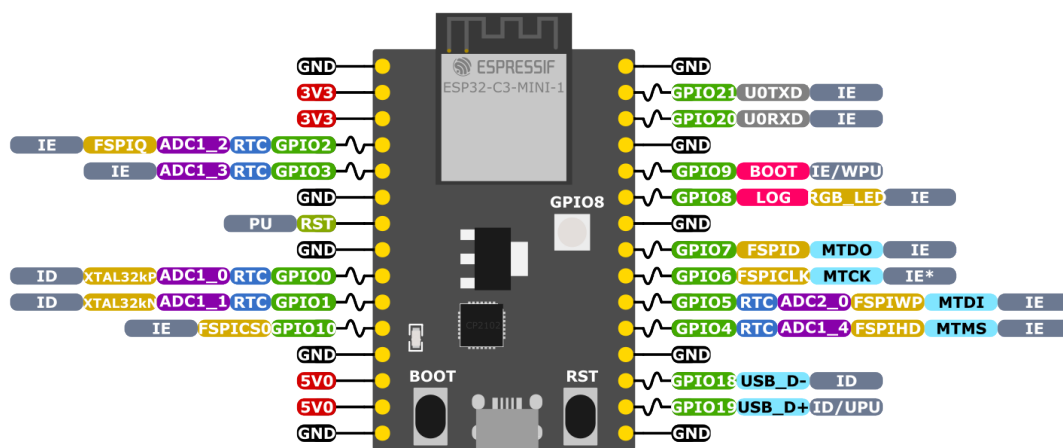
- [Build Secure and Cost-effective Connected Devices with ESP32-C3](#)
- [ESP32-C3 Datasheet \(PDF\)](#)
- [ESP32-C3-MINI-1 Datasheet \(PDF\)](#)
- [ESP32-C3-DevKitM-1 Schematic \(PDF\)](#)
- [ESP32-C3-DevKitM-1 PCB Layout \(PDF\)](#)
- [ESP32-C3-DevKitM-1 Dimensions \(PDF\)](#)
- [ESP32-C3-DevKitM-1 Dimensions source file \(DXF\)](#) - You can view it with [Autodesk Viewer](#) online

For further design documentation for the board, please contact us at sales@espressif.com.

¹ P: Power supply; I: Input; O: Output; T: High impedance.

² GPIO2, GPIO8, and GPIO9 are strapping pins of the ESP32-C3FN4 chip. These pins are used to control several chip functions depending on binary voltage values applied to the pins during chip power-up or system reset. For description and application of the strapping pins, please refer to Section Strapping Pins in [ESP32-C3 Datasheet](#).

ESP32-C3-DevKitM-1



ESP32-C3 Specs

32-bit RISC-V single-core @160MHz
 Wi-Fi IEEE 802.11 b/g/n 2.4GHz
 Bluetooth LE 5
 400 KB SRAM (16 KB for cache)
 384 KB ROM
 22 GPIOs, 3x SPI, 2x UART, I2C,
 I2S, RMT, LED PWM, USB Serial/JTAG,
 GDMA, TWAI®, 12-bit ADC

PWM Capable Pin
 GPIO Input and Output
 JTAG for Debugging and USB
 External Flash Memory (SPI)
 Analog-to-Digital Converter
 Other Related Functions
 Serial for Debug/Programming
 Strapping Pin Functions

RTC Power Domain (VDD3P3_RTC)
 Ground
 Power Rails (3V3 and 5V)

GPIO STATE:
UPU: USB Weak Pull-up
WPU: Weak Pull-up (Internal)
WPD: Weak Pull-down (Internal)
PU: Pull-up (External)
IE: Input Enable (After Reset)
IE*: Input Enable (Depends of FUSE_DIS_PAD_JTAG)
ID: Input Disabled (After Reset)
OE: Output Enable (After Reset)
OD: Output Disabled (After Reset)

Fig. 4: ESP32-C3-DevKitM-1 Pin Layout (click to enlarge)